ANNIE1-electronics-design - Issues

#	Tracker	Status	Priority	Subject	Assignee	Updated
10531	Task	Closed	Normal	Get two more MT boards from Wah	Matthew Wetstein	12/17/2015 05:23 PM
10469	Task	Closed	Normal	Order rack enclosure		12/18/2015 02:43 PM
10466	Task	Closed	Normal	Get bulkhead connectors for pickoff/splitter enclosure	Jonathan Eisch	12/18/2015 02:43 PM
10465	Task	Closed	Normal	Get FNAL EE staff to signoff on the enclosure design		12/17/2015 05:03 PM
10464	Task	Closed	Normal	Get FNAL guidelines for HV pickoff/splitter	Jonathan Eisch	12/18/2015 02:43 PM
10462	Task	Closed	Normal	Order components for full-sized pickoff/splitter board	Jonathan Eisch	12/21/2015 01:03 PM
10460	Task	Closed	Normal	Produce full size pickoff/splitter board	Jonathan Eisch	12/17/2015 05:17 PM
10459	Task	Closed	Normal	Verify prototype design with FNAL EE staff	Jonathan Eisch	12/17/2015 05:16 PM
10458	Task	Closed	Normal	panelize prototype design	Jonathan Eisch	12/17/2015 05:13 PM
10455	Task	Closed	Normal	Test AC characteristics without HV	Jonathan Eisch	12/17/2015 05:14 PM
10454	Task	Closed	Normal	Test prototype pickoff board	Jonathan Eisch	12/17/2015 05:21 PM
10453	Task	Closed	Normal	pickoff prototype board population	Jonathan Eisch	12/18/2015 02:46 PM
10452	Task	Closed	Normal	pickoff prototype board production	Jonathan Eisch	12/18/2015 02:46 PM
10451	Task	Closed	Normal	board design	Jonathan Eisch	12/17/2015 05:22 PM
10450	Task	Closed	Normal	Design and build prototype	Jonathan Eisch	12/17/2015 05:22 PM
10447	Task	Rejected	Normal	Cables for VME power supply	Jonathan Eisch	12/17/2015 05:11 PM
10446	Task	Closed	Normal	Test VME power supply	Jonathan Eisch	12/17/2015 05:10 PM
10444	Task	Closed	Normal	Power supply for VME crate	Jonathan Eisch	12/17/2015 05:11 PM
10441	Task	Closed	Normal	Readout events from two ADC cards	Jonathan Eisch	12/17/2015 05:08 PM
9679	Task	Closed	Normal	Pick a standard OS for DAQ operations	Jonathan Eisch	10/06/2015 01:46 PM
9087	Task	Closed	Normal	Get quotes for modern VME single-board computers	Jonathan Eisch	10/09/2015 01:11 PM
8907	Task	Closed	Normal	Ship PSEC eval card to QMUL		10/09/2015 01:11 PM
8814	Task	Closed	Normal	Splitting the PMT signal into the PSEC electronics	Jonathan Eisch	10/13/2015 04:36 PM
8807	Task	Closed	Normal	Get FADCs from UChicago		10/06/2015 01:45 PM
8804	Task	Closed	Normal	Follow up with PREP on LeCroy 1440s		10/09/2015 01:05 PM